

<b>INFORMATION DISCLOSURE CITATION</b> <small>(Use several sheets if necessary)</small>				Docket Number (Optional) 11675.1-1.1		Application Number 09/392,034	
				Applicant(s) Gonzalez, et al.		Filing Date September 8, 1999	
						Group Art Unit 2812	
<b>U.S. PATENT DOCUMENTS</b>							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/A.M./	1	3,791,882	Feb. 1974	Ogiue	438	417	
	2	4,409,609	Oct. 1983	Fukuda	257	520	
	3	4,717,682	Jan. 1988	Taka et al.	438	361	
	4	4,819,052	Apr. 1989	Hutter	257	378	
	5	4,965,221	Oct. 1990	Dennison et al.	437	70	
	6	5,045,904	Sep. 1991	Kobayashi et al.	257	539	
	7	5,221,857	Jun. 1993	Kano	257	539	
	8	5,229,316	Jul. 1993	Lee et al.	438	424	
	9	5,387,534	Feb. 1995	Prall	438	287	
	10	5,433,794	Jul. 1995	Fazan et al.	148	33	
	11	5,541,440	Jul. 1996	Kozai et al.	257	513	
<b>FOREIGN PATENT DOCUMENTS</b>							
REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
<b>OTHER DOCUMENTS</b> (Including Author, Title, Date, Pertinent Pages, Etc.)							
/A.M./	12	S. Wolf, Silicon Processing for the VLSI Era, Vol. 2; Process Integration, Lattice Press, pp. 54-56, 1990.					
/A.M./	13	Stanley Wolf, Silicon Processing for the VLSI Era, Vol. 2: Process Integration, Lattice Press, p. 331, 1990.					
EXAMINER	/Anh Mai/			DATE CONSIDERED	08/09/2007		
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

